

25/4/03  
FEB 26 2003

In re Application of: Katsumi Sameshima  
Serial No.: 09/451,979  
Confirmation No.: 9727  
Filed: November 30, 1999  
For: FERROELECTRIC MEMORY AND METHOD FOR  
MANUFACTURING SAME  
Commissioner for Patents  
Washington, D.C. 20231

Attorney Docket No.: 362-39 RCE

I hereby certify this correspondence is being deposited with the  
United States Postal Service as first class mail, postpaid in an  
envelope, addressed to: Commissioner for Patents,  
Washington, D.C. 20231 on

February 21, 2003

Signature: Marguerite Franco  
Marguerite Franco

RECEIVED  
MAR -3 2003  
TECHNOLOGY CENTER 2800

Sir:

Transmitted herewith is an Amendment in the above-identified application.

- Small entity status of this application under 37 C.F.R. 1.9 and 1.27 has been established by a verified statement previously submitted.
- A verified statement to establish small entity status under 37 C.F.R. 1.9 and 1.27 is enclosed.
- No additional fee is required

The fee has been calculated as shown below:

	(Col. 1)		(Col. 2)	(Col. 3)
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTR
TOTAL	5	MINUS	20	0
INDEP.	5	MINUS	5	0

X FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIMS

SMALL ENTITY		OR	OTHER THAN A SMALL ENTITY	
RATE	ADDL. FEE		RATE	ADDL. FEE
X 9 =	\$		x 18 =	
X 40 =	\$		x 84 =	
x 135 =	\$		x 270 =	
TOTAL	0		TOTAL	0

- Please charge my Deposit Account No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_. A duplicate copy of this sheet is attached.
- A check in the amount of \$ \_\_\_\_\_ is attached.
- The Commissioner is hereby authorized to charge any fees or additional fees associated with this communication or credit any overpayment to Deposit Account No. 502335. A duplicate copy of this sheet is attached.
- Any filing fees under 37 C.F.R. 1.16 for the presentation of extra claims.
- Any patent application processing fees under 37 C.F.R. 1.17.

Respectfully submitted,

Gerald T. Bodner

Gerald T. Bodner  
Registration No. 30,449

Bodner & O'Rourke, LLP  
425 Broadhollow Road  
Suite 108  
Melville, New York 11747



UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Katsumi Sameshima

Examiner: Wai-Sing Louie

Serial No.: 09/451,979

Group Art Unit: 2814

Filed: November 30, 1999

Docket: 362-39 RCE

For: FERROELECTRIC MEMORY AND  
METHOD FOR MANUFACTURING SAME

Confirmation No.: 9727

Commissioner for Patents  
Washington, D.C. 20231

Dated: February 21, 2003

I hereby certify this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to: Commissioner for Patents, Washington, D.C. 20231 on February 21, 2003.

Signature: Marguerite Franco  
Marguerite Franco

**REPLY TO FINAL OFFICE ACTION**

Sir:

In response to the Office Action mailed October 21, 2002, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please amend Claims 2 and 3 by rewriting the same as follows:

2. (Thrice Amended) A ferroelectric memory, comprising:  
an insulation film having a hollow at a top surface; and  
a laminated body obtained by laminating a plurality of layers on said top surface and etching a region of said plurality of layers corresponding to a region other than said hollow, wherein said laminated body includes a lower electrode layer, a ferroelectric layer formed on said lower electrode layer and an upper electrode layer formed on said ferroelectric layer; and the memory further comprising another film embedded from said top surface of said

bottom and separating between said insulation film and said lower electrode layer in said

TECHNOLoGIC CENTER 2800

RECEIVED

MAR -3 2003